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** CONTINUING DATA *****

This application is a CON of 09/903,580 07/13/2001 PAT 6,632,744

** FOREIGN APPLICATIONS *****

JAPAN 2000-215093 07/14/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/27/2004

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| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR | SHEETS | TOTAL | INDEPENDENT |
| 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | COUNTRY | DRAWING | CLAIMS | CLAIMS |
| Verified and Acknowledged Examiner's Signature <u>ICAD</u> Initials | JAPAN | 52 | 3 | 1 |

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TITLE

Manufacturing method of semiconductor integrated circuit device

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